

RS1G06 Single Inverter Buffer/Driver with Open-Drain Output

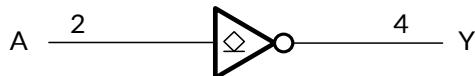
1 FEATURES

- **Operating Voltage Range: 1.65V to 5.5V**
- **Low Power Consumption: 1 μ A (Max)**
- **Operating Temperature Range: -40°C to +125°C**
- **Input and Open-Drain Output accept Voltage to 5.5V**
- **High Output Drive: ± 24 mA at V_{CC}=3.0V**
- **Micro SIZE PACKAGES: SOT23-5, SC70-5**

2 APPLICATIONS

- **Blu-ray Players and Home Theaters**
- **Desktops or Notebook PCs**
- **Digital Video Cameras (DVC)**
- **Mobile Phones**
- **Personal Navigation Device (GPS)**
- **Portable Media Player**

Functional Block Diagram



3 DESCRIPTIONS

The RS1G06 Single inverter buffer and driver is designed for 1.65V to 5.5V V_{CC} operation.

The RS1G06 device is open drain and can be connected to other open-drain outputs to implement active-low wired-OR or active-high wired-AND functions. The device is fully specified for partial-power-down applications using I_{OFF}. The I_{OFF} circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

The RS1G06 is available in Green SOT23-5 and SC70-5 packages. It operates over an ambient temperature range of -40°C to +125°C.

Device Information ⁽¹⁾

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS1G06	SOT23-5	2.92mm×1.60mm
	SC70-5	2.10mm×1.25mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

4 FUNCTION TABLE

INPUT	OUTPUT
A	Y
H	L
L	Z

H=High Voltage Level

L=Low Voltage Level

Z=High-impedance OFF-state

Table of Contents

1 FEATURES	1
2 APPLICATIONS	1
3 DESCRIPTIONS	1
4 FUNCTION TABLE	1
5 Revision History	3
6 PACKAGE/ORDERING INFORMATION ⁽¹⁾	4
7 PIN CONFIGURATIONS	5
8 SPECIFICATIONS.....	6
8.1 Absolute Maximum Ratings ⁽¹⁾	6
8.2 ESD Ratings	6
9 ELECTRICAL CHARACTERISTICS.....	7
9.1 Recommended Operating Conditions.....	7
9.2 DC Characteristics	8
9.3 AC Characteristics.....	8
10 Parameter Measurement Information	9
11 PACKAGE OUTLINE DIMENSIONS	10
12 TAPE AND REEL INFORMATION.....	12

5 Revision History

Note: Page numbers for previous revisions may different from page numbers in the current version.

Version	Change Date	Change Item
A.1	2022/04/28	Initial version completed
A.1.1	2024/02/28	1. Add MSL on Page 4@RevA.1 2. Modify packaging naming

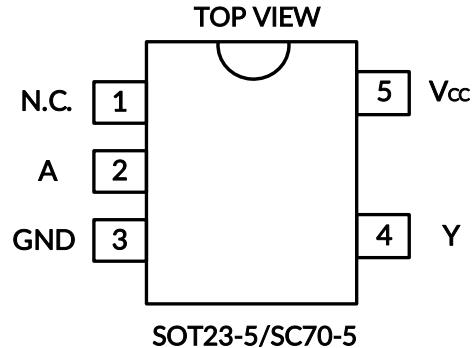
6 PACKAGE/ORDERING INFORMATION ⁽¹⁾

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING ⁽²⁾	MSL ⁽³⁾	PACKAGE OPTION
RS1G06	RS1G06XC5	-40°C ~+125°C	SC70-5 ⁽⁴⁾	1G06	MSL3	Tape and Reel,3000
	RS1G06XF5	-40°C ~+125°C	SOT23-5	1G06	MSL3	Tape and Reel,3000

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information(data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.
- (4) Equivalent to SOT353.

7 PIN CONFIGURATIONS



PIN DESCRIPTION

PIN	NAME	I/O TYPE⁽¹⁾	FUNCTION
SOT23-5/SC70-5			
1	N.C.	-	Not connected
2	A	I	Input
3	GND	P	Ground
4	Y	O	Output
5	V _{CC}	P	Power Pin

(1) I=Input, O=Output, P=Power.

8 SPECIFICATIONS

8.1 Absolute Maximum Ratings ⁽¹⁾

over operating free-air temperature range (unless otherwise noted) ⁽¹⁾⁽²⁾

			MIN	MAX	UNIT
V _{CC}	Supply voltage range		-0.5	6.5	V
V _I	Input voltage range ⁽²⁾		-0.5	6.5	V
V _O	Voltage range applied to any output in the high-impedance or power-off state ⁽²⁾		-0.5	6.5	V
V _O	Voltage range applied to any output in the high or low state ⁽²⁾⁽³⁾		-0.5	6.5	V
I _{IK}	Input clamp current	V _I <0		-50	mA
I _{OK}	Output clamp current	V _O <0		-50	mA
I _O	Continuous output current			±50	mA
	Continuous current through V _{CC} or GND			±100	mA
θ _{JA}	Package thermal impedance ⁽⁴⁾	SOT23-5		230	°C/W
		SC70-5		380	
T _J	Junction temperature ⁽⁵⁾		-65	150	°C
T _{STG}	Storage temperature		-65	150	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

(3) The value of V_{CC} is provided in the *Recommended Operating Conditions table*.

(4) The package thermal impedance is calculated in accordance with JESD-51.

(5) The maximum power dissipation is a function of T_{J(MAX)}, R_{θJA}, and T_A. The maximum allowable power dissipation at any ambient temperature is P_D = (T_{J(MAX)} - T_A) / R_{θJA}. All numbers apply for packages soldered directly onto a PCB.

8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

		VALUE	UNIT
V _(ESD)	Electrostatic discharge	Human-body model (HBM)	±6000 V
		Charged device model (CDM)	±1500 V
		Machine model (MM)	±200 V



ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

9 ELECTRICAL CHARACTERISTICS

over recommended operating free-air temperature range (Full=-40°C to +125°C, typical values are at $T_A = +25^\circ\text{C}$, unless otherwise noted.)⁽¹⁾

9.1 Recommended Operating Conditions

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
Supply voltage	V_{CC}	Operating	1.65	5.5	V
		Data retention only	1.5		
High-level input voltage	V_{IH}	$V_{CC}=1.65\text{V}$ to 1.95V	$0.65 \times V_{CC}$		V
		$V_{CC}=2.3\text{V}$ to 2.7V	1.7		
		$V_{CC}=3\text{V}$ to 3.6V	2.2		
		$V_{CC}=4.5\text{V}$ to 5.5V	$0.7 \times V_{CC}$		
Low-level input voltage	V_{IL}	$V_{CC}=1.65\text{V}$ to 1.95V		$0.15 \times V_{CC}$	V
		$V_{CC}=2.3\text{V}$ to 2.7V		0.3	
		$V_{CC}=3\text{V}$ to 3.6V		0.4	
		$V_{CC}=4.5\text{V}$ to 5.5V		$0.15 \times V_{CC}$	
Input voltage	V_I		0	5.5	V
Output voltage	V_O		0	5.5	V
Input transition rise or fall	$\Delta t/\Delta v$	$V_{CC}=1.8\text{V} \pm 0.15\text{V}, 2.5\text{V} \pm 0.2\text{V}$		20	ns/V
		$V_{CC}=3.3\text{V} \pm 0.3\text{V}$		10	
		$V_{CC}=5\text{V} \pm 0.5\text{V}$		5	
Operating temperature	T_A		-40	+125	°C

(1) All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation.

9.2 DC Characteristics

PARAMETER		TEST CONDITIONS	V _{cc}	TEMP	MIN ⁽²⁾	TYP ⁽³⁾	MAX ⁽²⁾	UNIT
V _{OL}	I _{OL} = 100µA		1.65V to 5.5V	Full			0.1	V
	I _{OL} = 4mA		1.65V				0.45	
	I _{OL} = 8mA		2.3V				0.3	
	I _{OL} = 16mA		3V				0.4	
	I _{OL} = 24mA						0.55	
	I _{OL} = 32mA		4.5V				0.55	
I _I	A input	V _I =5.5V or GND	0V to 5.5V	+25°C		±0.1	±1	µA
				Full			±5	
I _{off}		V _I or V _O =5.5V	0	+25°C		±0.1	±1	µA
				Full			±10	
I _{ICC}		V _I =5.5V or GND, I _O =0	1.65V to 5.5V	+25°C		0.1	1	µA
				Full			10	
ΔI _{CC}	One input at V _{CC} -0.6V, Other inputs at V _{CC} or GND		3V to 5.5V	Full			500	µA

(1) All unused inputs of the device must be held at V_{cc} or GND to ensure proper device operation.

(2) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

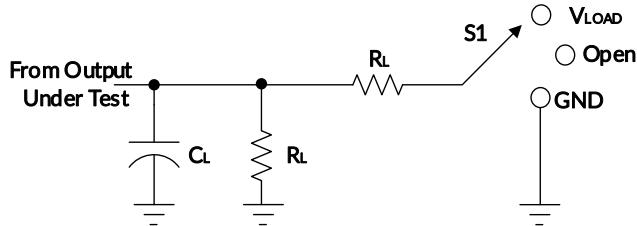
(3) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

9.3 AC Characteristics

PARAMETER	SYMBOL	TEST CONDITIONS		TEMP	MIN	TYP	MAX	UNIT
Propagation Delay	t _{pd}	V _{CC} =1.8V±0.15V	C _L =30pF, R _L =1kΩ	Full		6.4		ns
		V _{CC} =2.5V±0.2V	C _L =30pF, R _L =500Ω	Full		4.5		
		V _{CC} =3.3V±0.3V	C _L =50pF, R _L =500Ω	Full		4.2		
		V _{CC} =5V±0.5V	C _L =50pF, R _L =500Ω	Full		3.7		
Input Capacitance	C _i	V _{CC} =3.3V	V _I =V _{CC} or GND	+25°C		4		pF
Power dissipation capacitance	C _{pd}	V _{CC} =1.8V	f=10MHz	+25°C		3		pF
		V _{CC} =2.5V				3		
		V _{CC} =3.3V				4		
		V _{CC} =5V				6		

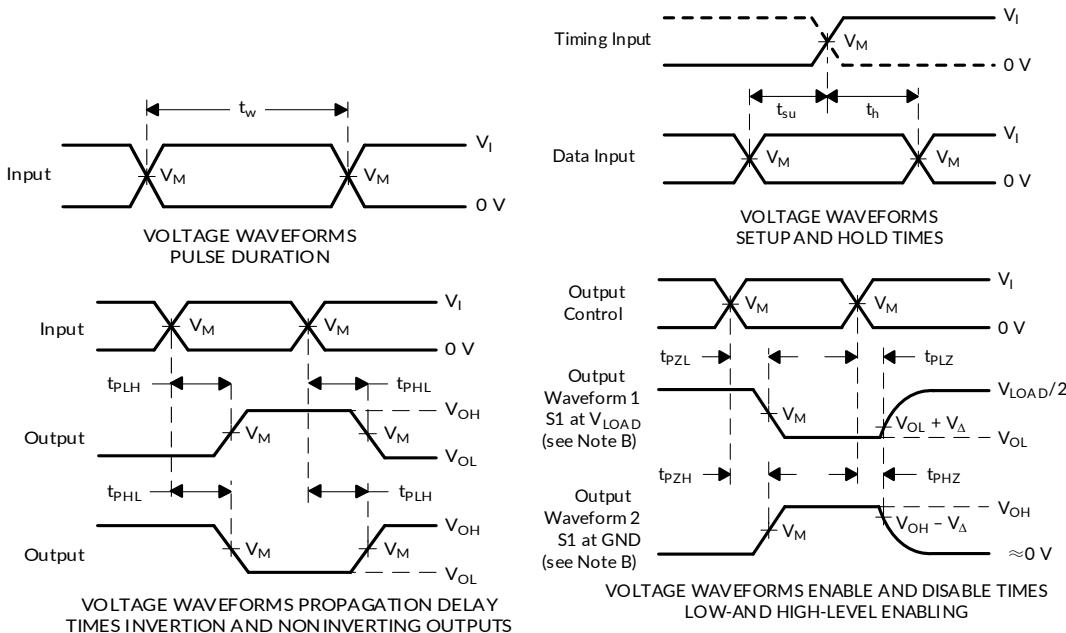
(1) All unused inputs of the device must be held at V_{cc} or GND to ensure proper device operation.

10 Parameter Measurement Information



TEST	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

V_{CC}	INPUTS		V_M	V_{LOAD}	C_L	R_L	V_Δ
	V_I	t_r/t_f					
$1.8V \pm 0.15V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	$30pF$	$1k\Omega$	$0.15V$
$2.5V \pm 0.2V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	$30pF$	500Ω	$0.15V$
$3.3V \pm 0.3V$	$3V$	$\leq 2.5ns$	$1.5V$	$6V$	$50pF$	500Ω	$0.3V$
$5V \pm 0.5V$	V_{CC}	$\leq 2.5ns$	$V_{CC}/2$	$2 \times V_{CC}$	$50pF$	500Ω	$0.3V$



NOTES: A. C_L includes probe and jig capacitance.

B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics: $PRR \leq 10$ MHz, $Z_0 = 50\Omega$.

D. The outputs are measured one at a time, with one transition per measurement.

E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .

F. t_{PLZ} and t_{PZH} are the same as t_{en} .

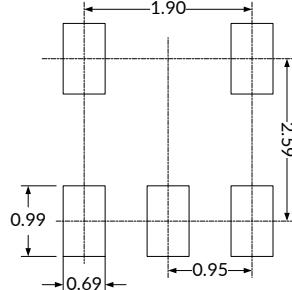
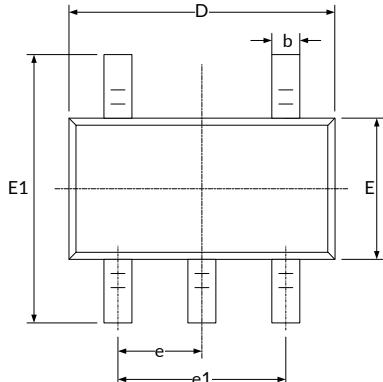
G. t_{PLH} and t_{PHL} are the same as t_{pd} .

H. All parameters and waveforms are not applicable to all devices.

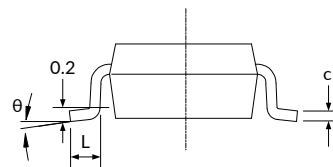
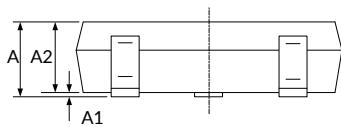
Figure 1. Load Circuit and Voltage Waveforms

11 PACKAGE OUTLINE DIMENSIONS

SOT23-5⁽³⁾



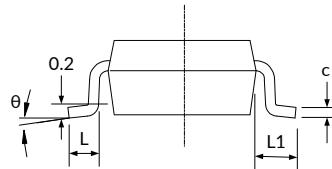
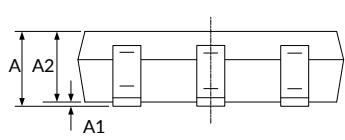
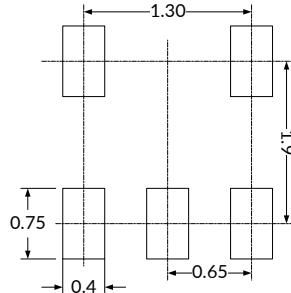
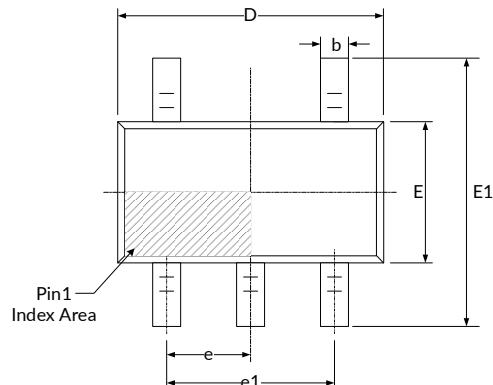
RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D ⁽¹⁾	2.820	3.020	0.111	0.119
E ⁽¹⁾	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC) ⁽²⁾		0.037(BSC) ⁽²⁾	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

SC70-5⁽³⁾


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A ⁽¹⁾	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D ⁽¹⁾	2.000	2.200	0.079	0.087
E ⁽¹⁾	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650(BSC) ⁽²⁾		0.026(BSC) ⁽²⁾	
e1	1.300(BSC) ⁽²⁾		0.051(BSC) ⁽²⁾	
L	0.260	0.460	0.010	0.018
L1	0.525		0.021	
θ	0°	8°	0°	8°

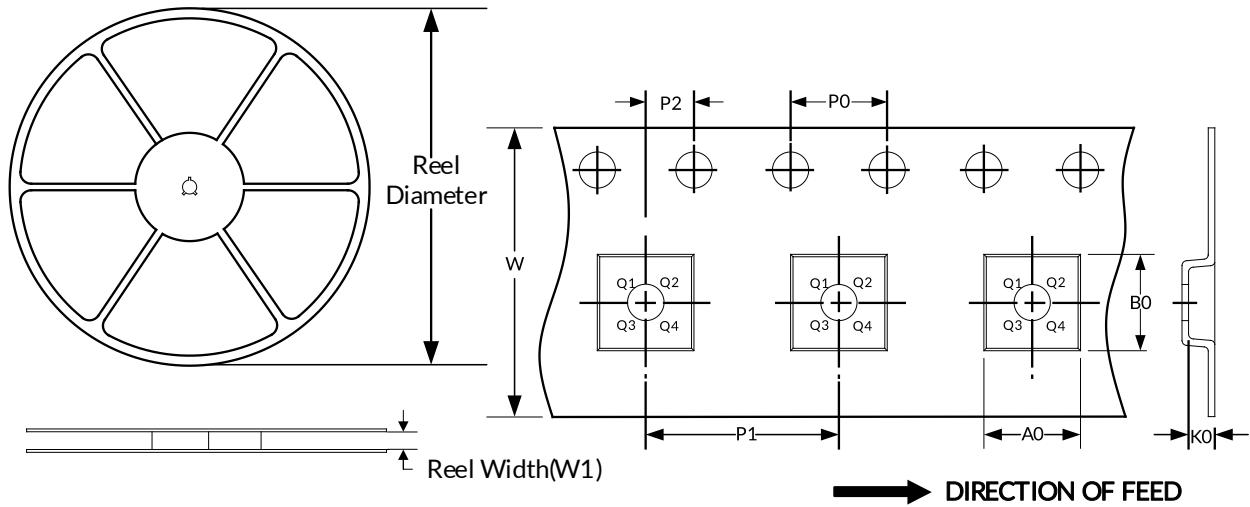
NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. This drawing is subject to change without notice.

12 TAPE AND REEL INFORMATION

REEL DIMENSIONS

TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width(mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SC70-5	7"	9.5	2.25	2.55	1.20	4.0	4.0	2.0	8.0	Q3
SOT23-5	7"	9.5	3.20	3.20	1.40	4.0	4.0	2.0	8.0	Q3

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

IMPORTANT NOTICE AND DISCLAIMER

Jiangsu RUNIC Technology Co., Ltd. will accurately and reliably provide technical and reliability data (including data sheets), design resources (including reference designs), application or other design advice, WEB tools, safety information and other resources, without warranty of any defect, and will not make any express or implied warranty, including but not limited to the warranty of merchantability Implied warranty that it is suitable for a specific purpose or does not infringe the intellectual property rights of any third party.

These resources are intended for skilled developers designing with RUNIC products You will be solely responsible for: (1) Selecting the appropriate products for your application; (2) Designing, validating and testing your application; (3) Ensuring your application meets applicable standards and any other safety, security or other requirements; (4) RUNIC and the RUNIC logo are registered trademarks of RUNIC INCORPORATED. All trademarks are the property of their respective owners; (5) For change details, review the revision history included in any revised document. The resources are subject to change without notice. Our company will not be liable for the use of this product and the infringement of patents or third-party intellectual property rights due to its use.