

## 4PST Depletion Mode Isolation Switch

### 1 FEATURES

- 4PST(NC)
- Depletion Mode MOSFETs
- Audio Frequency Range
- Supply Range: +1.6V to +3.0V
- Low ON-State Resistance: 0.5Ω (TYP)
- R<sub>ON</sub> Flat: 0.01 Ω Typical
- THD+N: 0.002% Typical
- Extended Industrial Temperature Range: -40°C to +85°C
- Available in Green WLCSP1.6X1.2-12 and QFN3X3-16 Package

### 3 DESCRIPTIONS

The RS550 is a high-performance four-pole single throw(4PST) normally closed Depletion-Mode isolation switch. The Depletion Mode technology allows the device to conduct signals when there is no V<sub>CC</sub> available and to isolate the signals when V<sub>CC</sub> is present.

The RS550 operates on a wide V<sub>CC</sub> range for design flexibility. Additionally, select pins allow the internal oscillator frequency to be adjusted between 500 kHz and 750 kHz in 75 kHz steps when V<sub>CC</sub> is present. This feature is used to shift the electromagnetic interference (EMI) signature to meet customer specifications.

### 2 APPLICATIONS

- MP3 Portable Media Players
- Cell Phones

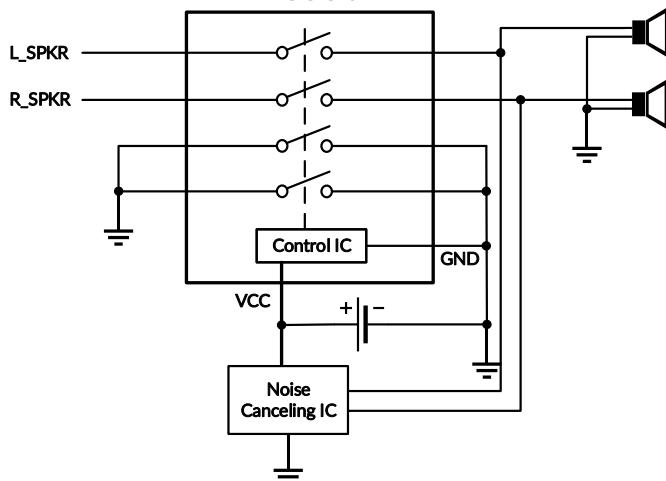
#### Device Information <sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS550	WLCSP1.6X1.2-12	1.60mm×1.20mm
	QFN3X3-16	3.00mm×3.00mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

### 4 TYPICAL APPLICATION CIRCUIT

RS550



---

## Table of Contents

<b>1 FEATURES .....</b>	1
<b>2 APPLICATIONS .....</b>	1
<b>3 DESCRIPTIONS .....</b>	1
<b>4 TYPICAL APPLICATION CIRCUIT .....</b>	1
<b>5 REVISION HISTORY .....</b>	3
<b>6 PACKAGE/ORDERING INFORMATION <sup>(1)</sup> .....</b>	4
<b>7 PIN CONFIGURATIONS .....</b>	5
<b>8 SPECIFICATIONS .....</b>	7
8.1 Absolute Maximum Ratings .....	7
8.2 ESD Ratings .....	7
8.3 Recommended Operating Conditions.....	7
8.4 Electrical Characteristics.....	8
<b>9 PACKAGE OUTLINE DIMENSIONS .....</b>	9
<b>10 TAPE AND REEL INFORMATION .....</b>	11

## 5 REVISION HISTORY

Note: Page numbers for previous revisions may different from page numbers in the current version.

VERSION	Change Date	Change Item
A.1	2019.6.1	Initial version completed
A.2	2019.9.10	Added UQFN2.5x2.5-12 package
B.1	2020.8.1	Added QFN3x3-16 package
B.2	2020.11.12	Deleted UQFN2.5x2.5-12 package
B.2.1	2024/02/23	Modify packaging naming
B.3	2024/05/28	<ol style="list-style-type: none"><li>1. Add Device Information on Page 1@RevB.2.1</li><li>2. Add MSL on Page 6@RevB.2.1</li><li>3. Add Package thermal impedance on Page 5@RevB.2.1</li></ol>

## 6 PACKAGE/ORDERING INFORMATION<sup>(1)</sup>

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING <sup>(2)</sup>	MSL <sup>(3)</sup>	PACKAGE OPTION
RS550	RS550YUCM12	-40°C ~+85°C	WLCSP1.6X1.2-12	RS550	MSL3	Tape and Reel, 3000
	RS550YTQC16	-40°C ~+85°C	QFN3X3-16	RS550	MSL3	Tape and Reel, 5000

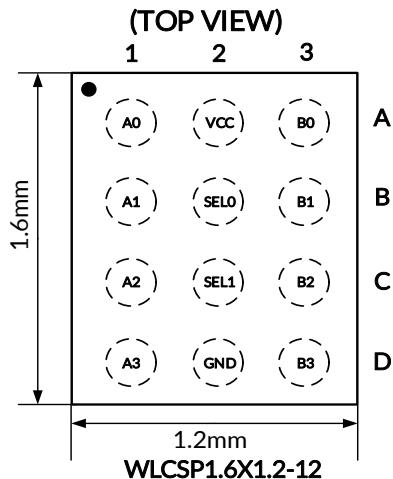
NOTE:

(1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.

(2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.

(3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

## 7 PIN CONFIGURATIONS

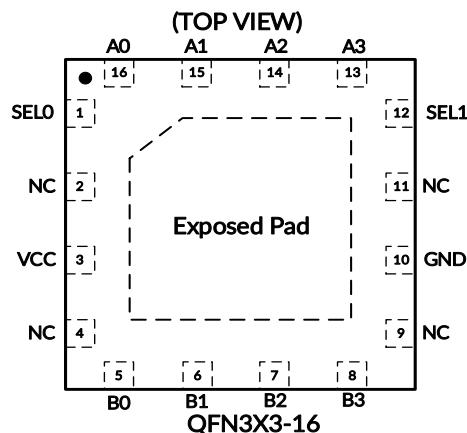


## PIN DESCRIPTION

<b>PIN#</b>	<b>NAME</b>	<b>TYPE<sup>(1)</sup></b>	<b>DESCRIPTION</b>
A1	A0	I/O	A-Port
B1	A1	I/O	A-Port
C1	A2	I/O	A-Port
D1	A3	I/O	A-Port
A2	VCC	P	Supply Voltage (see Table 1)
B2	SEL0	I	Oscillator Frequency Control (see Table 2). Used to shift the electromagnetic interference (EMI) signature to meet the customer specifications.
C2	SEL1	I	
D2	GND	G	Ground
A3	B0	I/O	B-Port
B3	B1	I/O	B-Port
C3	B2	I/O	B-Port
D3	B3	I/O	B-Port

(1) I = Input, O = Output, P=Power, G=Ground.

## PIN CONFIGURATIONS



## PIN DESCRIPTION

PIN#	NAME	TYPE <sup>(1)</sup>	DESCRIPTION
1	SEL0	I	Oscillator Frequency Control (see Table 2). Used to shift the electromagnetic interference (EMI) signature to meet the customer specifications.
12	SEL1	I	
2	NC	-	No Connect.
3	VCC	P	Supply Voltage (see Table 1)
4	NC	-	No Connect.
5	B0	I/O	B-Port
6	B1	I/O	B-Port
7	B2	I/O	B-Port
8	B3	I/O	B-Port
9	NC	-	No Connect.
10	GND	G	Ground
11	NC	-	No Connect.
13	A3	I/O	A-Port
14	A2	I/O	A-Port
15	A1	I/O	A-Port
16	A0	I/O	A-Port
--	Exposed Pad	G	Ground or float

(1) I = Input, O = Output, P=Power, G=Ground.

**Table 1. Truth Table**

V <sub>cc</sub>	Function
0V~0.2V	Conduction; B0~B3=A0~A3
1.6V~3.0V	Disconnect; B0~B3≠A0~A3

**Table 2. Oscillator Frequency Step Logic**

SEL1	SEL0	Frequency (TYP)
LOW	LOW	500kHz
LOW	HIGH	575kHz
HIGH	LOW	650kHz
HIGH	HIGH	725kHz

## 8 SPECIFICATIONS

### 8.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

<b>SYMBOL</b>	<b>PARAMETER</b>		<b>MIN</b>	<b>MAX</b>	<b>UNIT</b>
$V_{CC}$	Supply/Control Voltage,		0	5.5	V
$V_{IN}$	Input Voltage (Select Pins SEL0, SEL1)		0	$V_{CC}$	
$V_{SW(ON)}$	DC Switch I/O Voltage (Switch Conducting)	$V_{CC}=0V$	-5.0	+5.0	
$V_{SW(OFF)}^{(2)}$	DC Switch I/O Voltage (Switch Isolated)	$V_{CC}=\text{Powered}$	-1.8	+3.0	
$I_{IK}$	DC Input Diode Current		-50		mA
$I_{sw}$	Switch I/O Current	$V_{CC}=0V$		350	
$I_{SWPEAK}$	Peak Switch Current	Pulsed at 1ms Duration, <10% Duty Cycle		500	
$\theta_{JA}$	Package thermal impedance <sup>(3)</sup>		QFN3X3-16	70	°C/W
$T_A$	Absolute Maximum Operating Temperature		-40	+85	°C
$T_{stg}$	Storage		-65	+150	

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not implied.

(2) When a switch is isolated (OFF),  $V_{SW}$  value must be <  $V_{CC}$ .

(3) The package thermal impedance is calculated in accordance with JESD-51.

### 8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

			<b>VALUE</b>	<b>UNIT</b>
$V_{(ESD)}$	Electrostatic discharge	Human-Body Model (HBM)	±4000	V
		Machine Model (MM)	±200	



#### ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 8.3 Recommended Operating Conditions

Over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

<b>SYMBOL</b>	<b>PARAMETER</b>		<b>MIN</b>	<b>MAX</b>	<b>UNIT</b>
$V_{CC(ON)}$	Supply Voltage with Switch Conducting		0	0.2	V
$V_{CC(OFF)}$	Supply Voltage with Switch Isolated		1.6	3.0	V
$V_{SW(ON)}$	DC Switch I/O Voltage (Switch Conducting)	$V_{CC}=0V$	-2.0	+2.0	V
$V_{SW(OFF)}$	DC Switch I/O Voltage (Switch Isolated)	$V_{CC}=1.6V \text{ to } 3.0V$	-1.6	+1.6	V

(1) The Recommended Operating Condition table defines the conditions for actual device operation. Recommended operating condition are specified to ensure optimal performance to the datasheet specifications.

## 8.4 Electrical Characteristics

Typical values at  $T_A = 25^\circ\text{C}$  unless otherwise specified.

PARAMETER	SYMBOL	CONDITIONS	$V_{cc}(\text{V})$	$T_A$	MIN <sup>(1)</sup>	TYP <sup>(2)</sup>	MAX <sup>(1)</sup>	UNITS
<b>DC Electrical Characteristics</b>								
Switch-to-GND Leakage Current (Switch Conducting)	$I_{ON}$	$A_n = -1.4 \text{ V to } 1.4 \text{ V}$ , $B_n = \text{Float}$	0	+25°C	0	0.3	2.0	μA
Switch-to-GND Leakage Current (Switch Isolated)	$I_{OFF}$	$A_n = 0.4 \text{ V to } 1.4 \text{ V}$ , $B_n = \text{Float}$	3	+25°C	0	0.5	1.0	μA
Switch On Resistance <sup>(3)</sup>	$R_{ON}$	$I_{SW} = \pm 24 \text{ mA}$ , $V_{SW} = -1.4 \text{ V to } +1.4 \text{ V}$	0	+25°C		0.5		Ω
On Resistance Flatness <sup>(3)</sup>	$R_{FLAT(ON)}$	$I_{SW} = \pm 24 \text{ mA}$ , $V_{SW} = -1.4 \text{ V to } +1.4 \text{ V}$	0	+25°C		0.01		Ω
Quiescent Supply Current	$I_{CC}$	$SEL0 = SEL1 = V_{CC}$	3	+25°C	0	40	60	μA
Input Voltage High (Select Pins) <sup>(4)</sup>	$V_{IH}$		3	+25°C	$0.8 * V_{CC}$			V
Input Voltage Low (Select Pins) <sup>(4)</sup>	$V_{IL}$		3	+25°C			$0.2 * V_{CC}$	V
Input Leakage Current (Select Pins)	$I_{IN}$		3	+25°C	0		±1	μA
<b>AC Electrical Characteristics</b>								
Turn-On Time $V_{CC}$ to Output <sup>(5,6)</sup>	$t_{ON}$	$R_L = 32 \Omega$ , $C_L = 10 \text{ pF}$ , $V_{SW} = 1.4 \text{ V}$	1.6	+25°C		160		ns
Turn-Off Time $V_{CC}$ to Output <sup>(5,6)</sup>	$t_{OFF}$	$R_L = 32 \Omega$ , $C_L = 10 \text{ pF}$ , $V_{SW} = 1.4 \text{ V}$	1.6	+25°C		90		μs
Off Isolation <sup>(5,6)</sup>	$O_{IRR}$	$R_L = 32 \Omega$ , $f = 20 \text{ kHz}$ , $V_{SW} = 0.35 V_{RMS}$	1.6	+25°C		-80		dB
Crosstalk <sup>(5,6)</sup>	$X_{TALK}$	$R_L = 32 \Omega$ , $f = 20 \text{ kHz}$ , $V_{SW} = 1 V_{RMS}$	0	+25°C		-90		dB
-3dB Bandwidth <sup>(5)</sup>	$BW$	$R_L = 50 \Omega$ , $C_L = 0 \text{ pF}$	0	+25°C		<200		MHZ
Total Harmonic Distortion + Noise <sup>(5,6)</sup>	THD+N	$R_L = 32 \Omega$ , $f = 20 \text{ Hz to } 20 \text{ kHz}$ , $V_{SW} = 1 V_{RMS}$	0	+25°C		0.002		%

(1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(2) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

(3) Guaranteed by test and characterization.

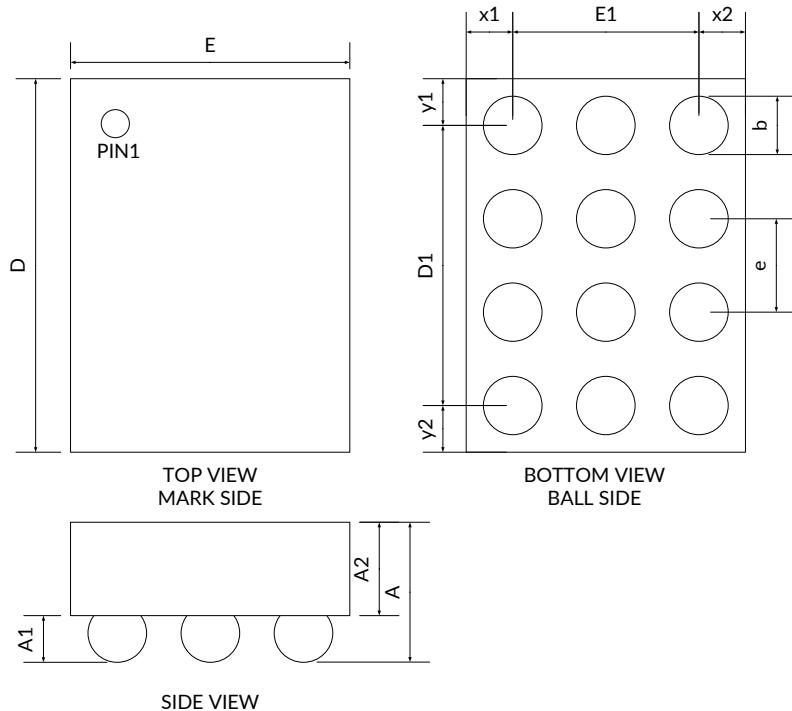
(4) Voltages on select control pins must be < $V_{CC}$ .

(5)  $SEL0=SEL1=\text{LOW}$ .

(6) Guaranteed by characterization.

## 9 PACKAGE OUTLINE DIMENSIONS

**WLCSP1.6X1.2-12<sup>(4)</sup>**

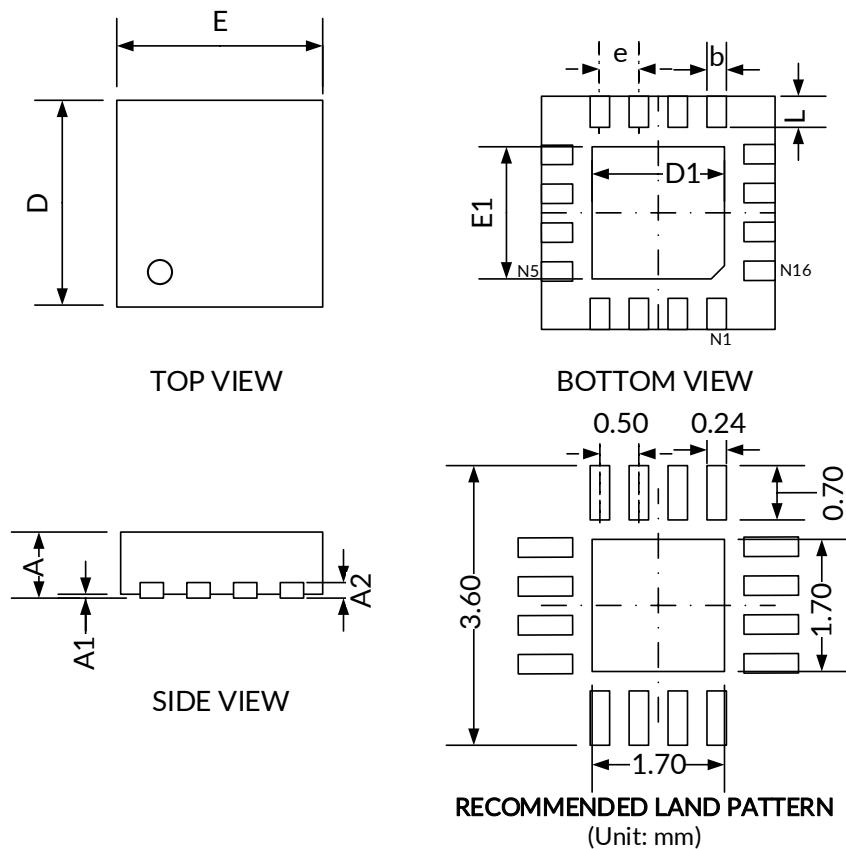


NOTES: ALL WAFER ORIENTATION NOTCH DOWN

SYMBOL	Dimensions In Millimeters			Dimensions In Inches		
	Min	Nom	Max	Min	Nom	Max
A <sup>(1)</sup>	0.542	0.582	0.622	0.021	0.023	0.024
A1	0.177	0.202	0.227	0.007	0.008	0.009
A2	0.355	0.380	0.405	0.014	0.015	0.016
D <sup>(1)</sup>	1.570	1.600	1.630	0.062	0.063	0.064
D1	1.200 BSC <sup>(2)</sup>			0.047 BSC <sup>(2)</sup>		
E <sup>(1)</sup>	1.170	1.200	1.230	0.046	0.047	0.048
E1	0.800 BSC <sup>(2)</sup>			0.031 BSC <sup>(2)</sup>		
b	0.243	0.268	0.293	0.010	0.011	0.012
e	0.400 BSC <sup>(2)</sup>			0.016 BSC <sup>(2)</sup>		
x1	0.185 REF <sup>(3)</sup>			0.007 REF <sup>(3)</sup>		
x2	0.185 REF <sup>(3)</sup>			0.007 REF <sup>(3)</sup>		
y1	0.185 REF <sup>(3)</sup>			0.007 REF <sup>(3)</sup>		
y2	0.185 REF <sup>(3)</sup>			0.007 REF <sup>(3)</sup>		

NOTE:

1. Plastic or metal protrusions of 0.075mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. REF is the abbreviation for Reference.
4. This drawing is subject to change without notice.

**QFN3X3-16<sup>(2)</sup>**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203		0.008	
b	0.180	0.300	0.007	0.012
D <sup>(1)</sup>	2.900	3.100	0.114	0.122
D1	1.600	1.800	0.063	0.071
E <sup>(1)</sup>	2.900	3.100	0.114	0.122
E1	1.600	1.800	0.063	0.071
e	0.500 TYP		0.020 TYP	
L	0.300	0.500	0.012	0.020

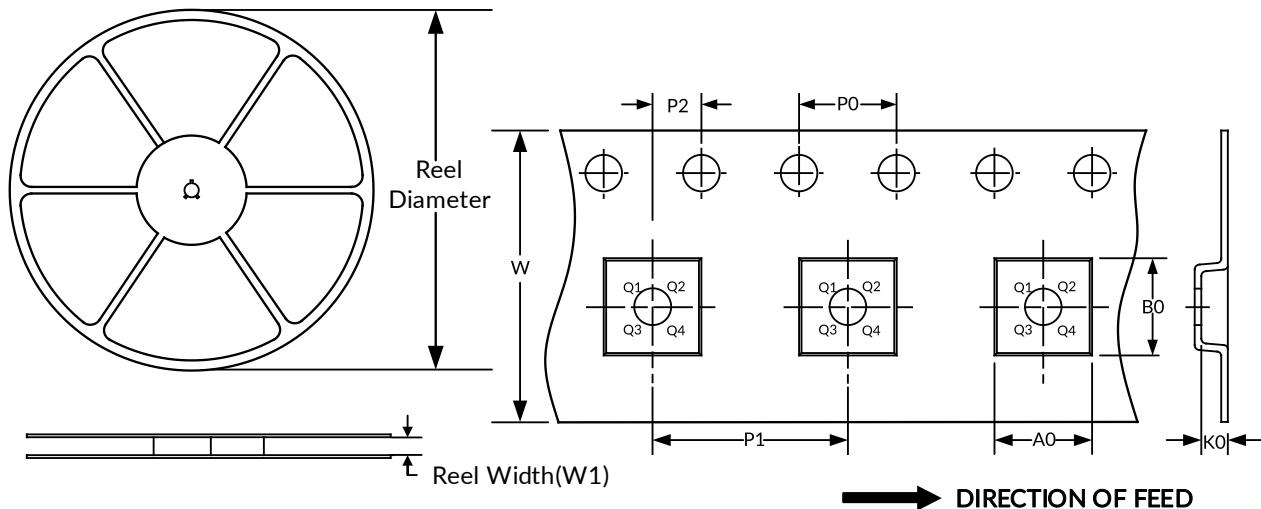
**NOTE:**

1. Plastic or metal protrusions of 0.075mm maximum per side are not included.
2. This drawing is subject to change without notice.

## 10 TAPE AND REEL INFORMATION

### REEL DIMENSIONS

### TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
QFN3X3-16	13"	12.4	3.35	3.35	1.13	4.0	8.0	2.0	12.0	Q1
WLCSP1.6X1.2-12	7"	8.3	1.35	1.75	0.70	4.0	4.0	2.0	8.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

## IMPORTANT NOTICE AND DISCLAIMER

Jiangsu RUNIC Technology Co., Ltd. will accurately and reliably provide technical and reliability data (including data sheets), design resources (including reference designs), application or other design advice, WEB tools, safety information and other resources, without warranty of any defect, and will not make any express or implied warranty, including but not limited to the warranty of merchantability Implied warranty that it is suitable for a specific purpose or does not infringe the intellectual property rights of any third party.

These resources are intended for skilled developers designing with RUNIC products You will be solely responsible for: (1) Selecting the appropriate products for your application; (2) Designing, validating and testing your application; (3) Ensuring your application meets applicable standards and any other safety, security or other requirements; (4) RUNIC and the RUNIC logo are registered trademarks of RUNIC INCORPORATED. All trademarks are the property of their respective owners; (5) For change details, review the revision history included in any revised document. The resources are subject to change without notice. Our company will not be liable for the use of this product and the infringement of patents or third-party intellectual property rights due to its use.