

# Octal Buffer/Driver With 3-State Outputs

## 1 FEATURES

- **Power-Supply Range: 1.65V to 5.5V**
- **V<sub>cc</sub> Isolation: If V<sub>cc</sub> is at GND, Both Ports are in the High-Impedance State**
- **I<sub>OFF</sub>: Supports Partial-Power-Down Mode Operation**
- **Extended Temperature: -40°C to +125°C**

## 2 APPLICATIONS

- Handset
- Smartphone
- Tablet
- Desktop PC

## 3 DESCRIPTIONS

This RS244S is an octal non-inverting buffer/driver with 3-state outputs. The 3-state outputs are controlled by the output enable inputs  $\overline{OE}$  and  $\overline{OE}$ . A HIGH on  $\overline{OE}$  causes the outputs to assume a high impedance OFF-state. V<sub>cc</sub> supporting operating voltage from 1.65 V to 5.5 V.

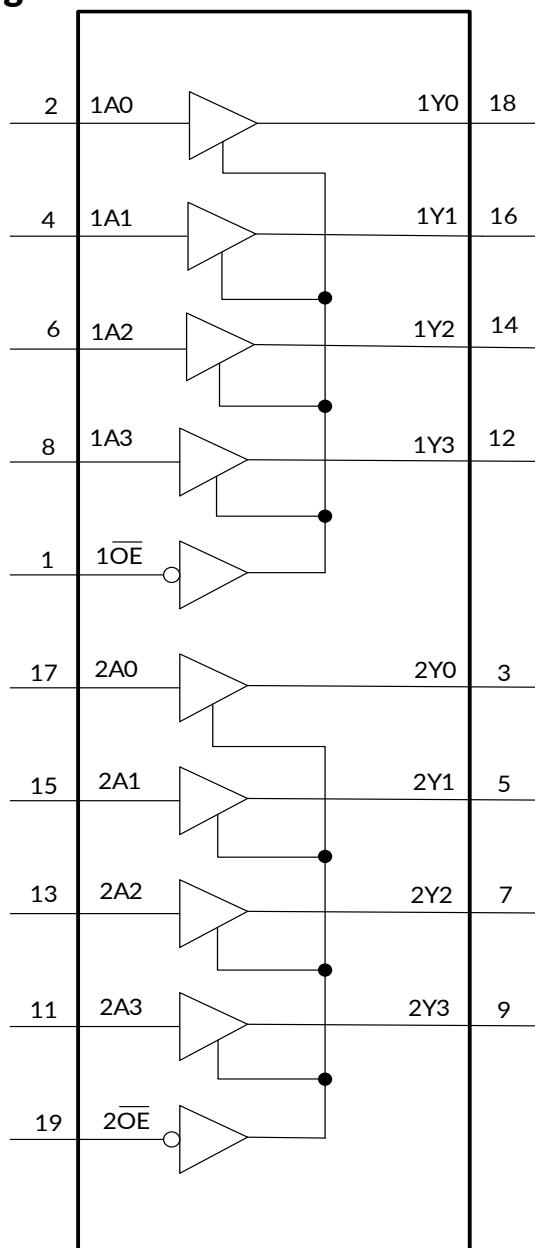
To ensure the high-impedance state during power up or power down,  $\overline{OE}$  should be tied to V<sub>cc</sub> through a pullup resistor, the minimum value of the resistor is determined by the current-sinking capability of the driver.

Device Information <sup>(1)</sup>

PART NUMBER	PACKAGE	BODY SIZE (NOM)
RS244S	TSSOP20	6.50mm×4.40mm
	SOP20	12.80mm×7.50mm

(1) For all available packages, see the orderable addendum at the end of the data sheet.

## 4 Functional Block Diagram



**Function Table**

INPUTS		OUTPUT	
$\overline{OE}$	A PORT	Y PORT	
L	H	H	
L	L	L	
H	X	Hi-Z	

NOTE:

H=HIGH voltage level

L=LOW voltage level

X=don't care

Z=high impedance OFF-state

## Table of Contents

<b>1 FEATURES .....</b>	1
<b>2 APPLICATIONS .....</b>	1
<b>3 DESCRIPTIONS .....</b>	1
<b>4 Functional Block Diagram.....</b>	2
<b>5 Revision History .....</b>	4
<b>6 PACKAGE/ORDERING INFORMATION <sup>(1)</sup> .....</b>	5
<b>7 PIN CONFIGURATIONS .....</b>	6
<b>8 SPECIFICATIONS.....</b>	7
8.1 Absolute Maximum Ratings .....	7
8.2 ESD Ratings .....	7
8.3 Recommended Operating Conditions.....	8
8.4 Electrical Characteristics.....	9
8.5 Switching Characteristics.....	10
8.6 Operating Characteristics .....	10
8.7 Typical Characteristics .....	10
<b>9 Parameter Measurement Information.....</b>	11
<b>10 PACKAGE OUTLINE DIMENSIONS .....</b>	12
<b>11 TAPE AND REEL INFORMATION.....</b>	14

## 5 Revision History

Note: Page numbers for previous revisions may different from page numbers in the current version.

VERSION	Change Date	Change Item
A.1	2023/08/23	Initial version completed
A.1.1	2024/02/26	Modify packaging naming
A.2	2024/05/17	Update KEY PARAMETER LIST OF TAPE AND REEL

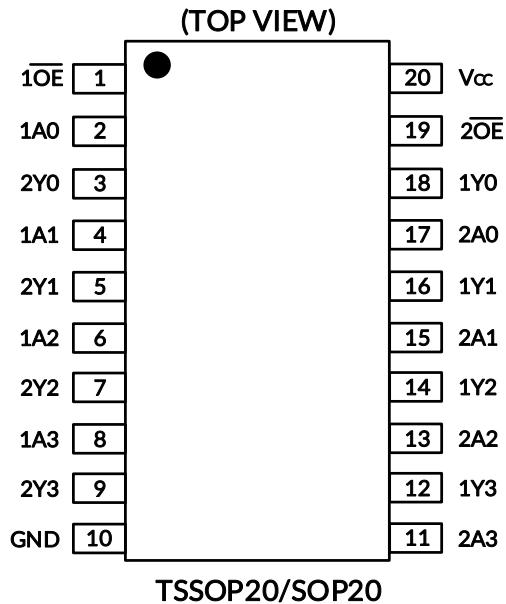
## 6 PACKAGE/ORDERING INFORMATION <sup>(1)</sup>

PRODUCT	ORDERING NUMBER	TEMPERATURE RANGE	PACKAGE LEAD	PACKAGE MARKING <sup>(2)</sup>	MSL <sup>(3)</sup>	PACKAGE OPTION
RS244S	RS244SXTSS20	-40°C ~+125°C	TSSOP20	RS244S	MSL3	Tape and Reel, 4000
	RS244SXS20	-40°C ~+125°C	SOP20	RS244S	MSL3	Tape and Reel, 1500

NOTE:

- (1) This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the right-hand navigation.
- (2) There may be additional marking, which relates to the lot trace code information (data code and vendor code), the logo or the environmental category on the device.
- (3) MSL, The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications.

## 7 PIN CONFIGURATIONS



## PIN DESCRIPTION

PIN	NAME	TYPE <sup>(1)</sup>	FUNCTION
TSSOP20/SOP20			
1	1 $\overline{OE}$	I	Output Enable (Active Low). Pull 1 $\overline{OE}$ high to place all outputs in 3-state mode.
2	1A0	I	Input
3	2Y0	O	Output
4	1A1	I	Input
5	2Y1	O	Output
6	1A2	I	Input
7	2Y2	O	Output
8	1A3	I	Input
9	2Y3	O	Output
10	GND	G	Ground.
11	2A3	I	Input
12	1Y3	O	Output
13	2A2	I	Input
14	1Y2	O	Output
15	2A1	I	Input
16	1Y1	O	Output
17	2A0	I	Input
18	1Y0	O	Output
19	2 $\overline{OE}$	I	Output Enable (Active Low). Pull 2 $\overline{OE}$ high to place all outputs in 3-state mode.
20	V <sub>CC</sub>	P	Supply voltage. 1.65V≤V <sub>CC</sub> ≤5.5V

(1) I=input, O=output, I/O=input and output, P=power

## 8 SPECIFICATIONS

### 8.1 Absolute Maximum Ratings

Over operating free-air temperature range (unless otherwise noted) <sup>(1)</sup>

SYMBOL	PARAMETER		MIN	MAX	UNIT
V <sub>CC</sub>	Supply Voltage Range		-0.5	6.5	V
V <sub>I</sub> <sup>(2)</sup>	Input Voltage Range	A port	-0.5	6.5	V
		Control inputs	-0.5	6.5	V
V <sub>O</sub> <sup>(2)</sup>	Voltage range applied to any output in the high-impedance or power-off state		Y port	-0.5	V
V <sub>O</sub> <sup>(2)(3)</sup>	Voltage range applied to any output in the high or low state		Y port	-0.5	V <sub>CC</sub> +0.5
I <sub>IK</sub>	Input clamp current		V <sub>I</sub> <0	-50	mA
I <sub>OK</sub>	Output clamp current		V <sub>O</sub> <0	-50	mA
I <sub>O</sub>	Continuous output current			±50	mA
	Continuous current through V <sub>CC</sub> or GND			±100	mA
θ <sub>JA</sub>	Package thermal impedance <sup>(4)</sup>	TSSOP20	40	°C/W	
		SOP20	40		
T <sub>J</sub>	Junction Temperature <sup>(5)</sup>		-40	150	°C
T <sub>STG</sub>	Storage temperature		-65	150	

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, which do not imply functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2) The input and output negative-voltage ratings may be exceeded if the input and output current ratings are observed.

(3) The value of V<sub>CC</sub> are provided in the recommended operating conditions table.

(4) The package thermal impedance is calculated in accordance with JESD-51.

(5) The maximum power dissipation is a function of T<sub>J(MAX)</sub>, R<sub>θJA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any ambient temperature is P<sub>D</sub> = (T<sub>J(MAX)</sub> - T<sub>A</sub>) / R<sub>θJA</sub>. All numbers apply for packages soldered directly onto a PCB.

### 8.2 ESD Ratings

The following ESD information is provided for handling of ESD-sensitive devices in an ESD protected area only.

		VALUE	UNIT
V <sub>(ESD)</sub>	Electrostatic discharge	Human-Body Model (HBM), MIL-STD-883K METHOD 3015.9	±2000 V
		Charged-Device Model (CDM), ANSI/ESDA/JEDEC JS-002-2018	±1000 V
		Machine Model (MM), JESD22-A115C (2010)	±200 V



#### ESD SENSITIVITY CAUTION

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

### 8.3 Recommended Operating Conditions

$V_{CC}$  is the supply voltage associated with the input port and output port. <sup>(1)(2)</sup>

PARAMETER		$V_{CC}$	MIN	TYP	MAX	UNIT
Supply voltage	$V_{CC}$		1.65		5.5	V
High-level input Voltage ( $V_{IH}$ )	Inputs <sup>(3)</sup>	1.65V to 1.95V	$V_{CC} \times 0.65$			V
		2.3V to 2.7V	1.7			
		3V to 3.6V	2			
		4.5V to 5.5V	$V_{CC} \times 0.7$			
Low-level input Voltage ( $V_{IL}$ )	Inputs <sup>(3)</sup>	1.65V to 1.95V			$V_{CC} \times 0.35$	V
		2.3V to 2.7V			0.7	
		3V to 3.6V			0.8	
		4.5V to 5.5V			$V_{CC} \times 0.3$	
Input voltage ( $V_I$ )	Input voltage		0		5.5	V
Output voltage ( $V_O$ )	Output voltage		0		$V_{CC}$	V
High-level output current ( $I_{OH}$ )		1.65V to 1.95V			-4	mA
		2.3V to 2.7V			-8	
		3V to 3.6V			-24	
		4.5V to 5.5V			-32	
Low-level output current ( $I_{OL}$ )		1.65V to 1.95V			4	mA
		2.3V to 2.7V			8	
		3V to 3.6V			24	
		4.5V to 5.5V			32	
Input transition rise or fall rate( $\Delta t/\Delta v$ )	Data inputs	1.65V to 1.95V			20	ns/V
		2.3V to 2.7V			20	
		3V to 3.6V			10	
		4.5V to 5.5V			5	
$T_A$ Operating free-air temperature			-40		125	°C

(1) All unused or driven (floating) data inputs (I/Os) of the device must be held at logic HIGH or LOW (preferably  $V_{CC}$  or GND) to ensure proper device operation and minimize power.

(2) All unused control inputs must be held at  $V_{CC}$  or GND to ensure proper device operation and minimize power consumption.

(3) For  $V_{CC}$  values not specified in the data sheet,  $V_{IH}$  min =  $V_{CC} \times 0.7$  V,  $V_{IL}$  max =  $V_{CC} \times 0.3$  V.

## 8.4 Electrical Characteristics

over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	CONDITIONS	V <sub>CC</sub>	TEMP	MIN <sup>(1)</sup>	TYP <sup>(2)</sup>	MAX <sup>(1)</sup>	UNIT
V <sub>OH</sub>	I <sub>OH</sub> = -100 µA V <sub>I</sub> =V <sub>IH</sub>	1.65V to 4.5V	Full	V <sub>CC</sub> -0.1			V
	I <sub>OH</sub> = -4mA V <sub>I</sub> =V <sub>IH</sub>	1.65V		1.2			
	I <sub>OH</sub> = -8mA V <sub>I</sub> =V <sub>IH</sub>	2.3V		1.9			
	I <sub>OH</sub> = -24mA V <sub>I</sub> =V <sub>IH</sub>	3V		2.2			
	I <sub>OH</sub> = -32mA V <sub>I</sub> =V <sub>IH</sub>	4.5V		3.6			
V <sub>OL</sub>	I <sub>OL</sub> = 100 µA V <sub>I</sub> =V <sub>IL</sub>	1.65V to 4.5V				0.1	V
	I <sub>OL</sub> = 4mA V <sub>I</sub> =V <sub>IL</sub>	1.65V				0.45	
	I <sub>OL</sub> = 8mA V <sub>I</sub> =V <sub>IL</sub>	2.3V				0.4	
	I <sub>OL</sub> = 24mA V <sub>I</sub> =V <sub>IL</sub>	3V				0.85	
	I <sub>OL</sub> = 32mA V <sub>I</sub> =V <sub>IL</sub>	4.5V				0.89	
I <sub>I</sub>	V <sub>I</sub> = 5.5V or GND	5.5V	+25°C			±1	µA
I <sub>off</sub>	V <sub>I</sub> or V <sub>O</sub> = 0 to 5.5V	0V	+25°C			±1	µA
			Full			±2	
I <sub>OZ</sub> <sup>(3)</sup>	V <sub>O</sub> = V <sub>CC</sub> or GND, OE=V <sub>IH</sub>	1.65V to 5.5V	+25°C			±1	µA
			Full			±2	
I <sub>CC</sub>	V <sub>CC</sub> supply current V <sub>I</sub> = V <sub>CC</sub> or GND <sup>(4)</sup> I <sub>O</sub> = 0	1.65V to 5.5V	+25°C			1	µA
			Full			5	
		0V	Full			-2	
ΔI <sub>CC</sub>	One A port at V <sub>CC</sub> – 0.6V, Y port = open	3V to 5.5V	Full			50	µA
C <sub>I</sub>	V <sub>I</sub> = V <sub>CC</sub> or GND	3.3V	+25°C		3.3		pF
C <sub>O</sub>	V <sub>O</sub> = V <sub>CC</sub> or GND	3.3V	+25°C		5		pF

(1) Limits are 100% production tested at 25°C. Limits over the operating temperature range are ensured through correlations using statistical quality control (SQC) method.

(2) Typical values represent the most likely parametric norm as determined at the time of characterization. Actual typical values may vary over time and will also depend on the application and configuration.

(3) For I/O ports, the parameter I<sub>OZ</sub> includes the input leakage current.

(4) Hold all unused data inputs of the device at V<sub>CCI</sub> or GND to assure proper device operation.

## 8.5 Switching Characteristics

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V <sub>CC</sub> =1.8V ±0.15V		V <sub>CC</sub> =2.5V ±0.2V		V <sub>CC</sub> =3.3V ±0.3V		V <sub>CC</sub> =5V ±0.5V		UNIT
			MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX	
t <sub>PLH</sub>	An	Yn	4.8	18	2.1	8.1	1.8	6.3	1.5	4.8	ns
t <sub>PHL</sub>											
t <sub>PHZ</sub>	$\overline{OE}$	Yn	4.8	20.7	2.3	8.7	2.1	8.5	1.3	6.2	ns
t <sub>PLZ</sub>											
t <sub>PZH</sub>	$\overline{OE}$	Yn	4.1	25.1	2.4	13.2	1.7	10.1	1.6	7.7	ns
t <sub>PZL</sub>											

## 8.6 Operating Characteristics

T<sub>A</sub>=25°C

PARAMETER	TEST CONDITIONS	V <sub>CC</sub> =1.8V		V <sub>CC</sub> =2.5V		V <sub>CC</sub> =3.3V		V <sub>CC</sub> =5V		UNIT
		TYP	TYP	TYP	TYP	TYP	TYP	TYP	TYP	
C <sub>pd</sub> <sup>(1)</sup>	Outputs enabled	C <sub>L</sub> =0, f=10MHz, t <sub>r</sub> =t <sub>f</sub> =1ns	8	9	11	13				pF
	Outputs disabled		1	1	1.5	2				

(1) Power dissipation capacitance per transceiver.

## 8.7 Typical Characteristics

NOTE: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only.

At T<sub>A</sub> = +25°C, V<sub>CC</sub>=5V, unless otherwise noted.

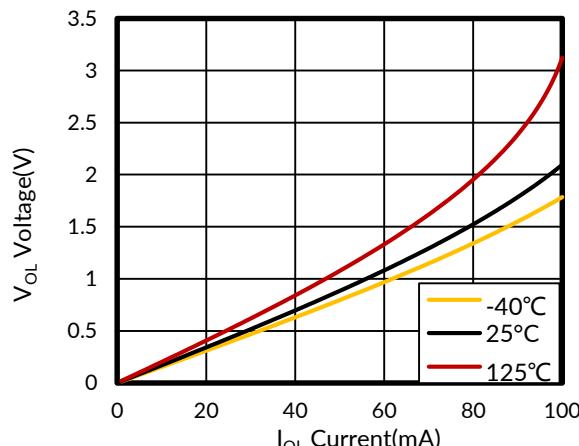


Figure 1. Voltage vs Current

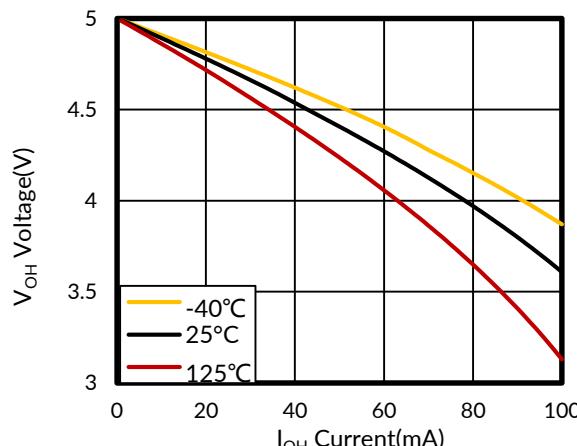
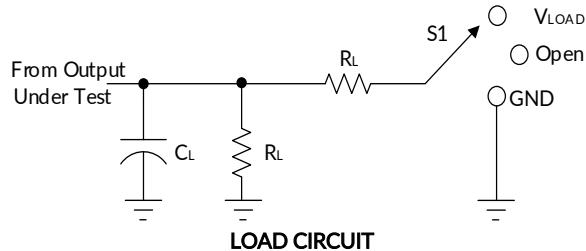


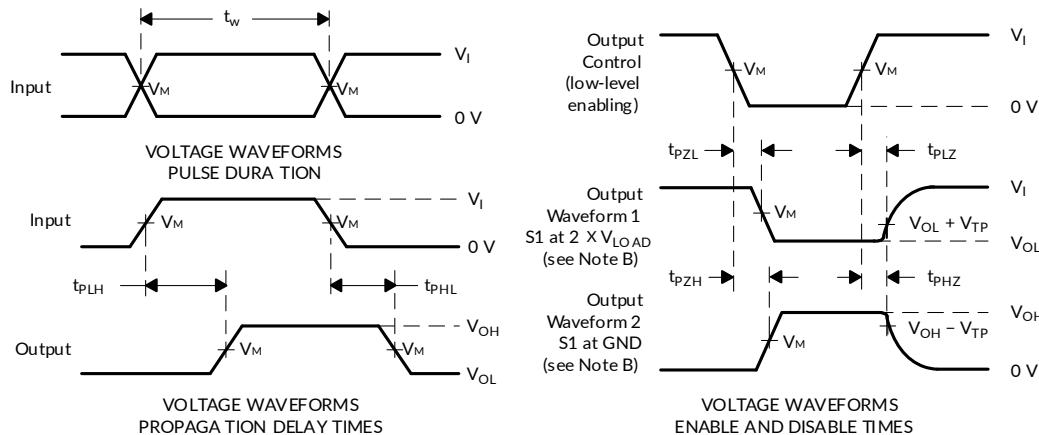
Figure 2. Voltage vs Current

## 9 Parameter Measurement Information



TEST	S1
$t_{pd}$	Open
$t_{PLZ}/t_{PZL}$	$V_{LOAD}$
$t_{PHZ}/t_{PZH}$	GND

<b>V<sub>cc</sub></b>	<b>V<sub>I</sub></b>	<b>V<sub>M</sub></b>	<b>C<sub>L</sub></b>	<b>R<sub>L</sub></b>	<b>V<sub>TP</sub></b>
1.8V±0.15V	V <sub>cc</sub>	V <sub>cc</sub> /2	15pF	2kΩ	0.15V
2.5V±0.2V	V <sub>cc</sub>	V <sub>cc</sub> /2	15pF	2kΩ	0.15V
3.3V±0.3V	2.7V	1.5V	15pF	2kΩ	0.3V
5V±0.5V	2.7V	1.5V	15pF	2kΩ	0.3V



NOTES: A. CL includes probe and jig capacitance.

B. Waveform 1 is for an output with internal conditions such that the output is low, except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high, except when disabled by the output control.

C. All input pulses are supplied by generators having the following characteristics: PRR≤10 MHz,  $Z_O = 50\Omega$ ,  $dv/dt \geq 1V/ns$ .

D. The outputs are measured one at a time, with one transition per measurement.

E.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .

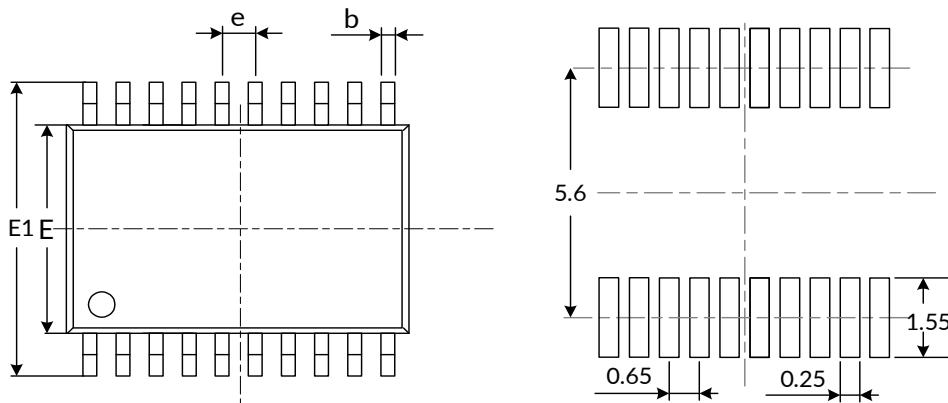
F.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .

G.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{pd}$ .

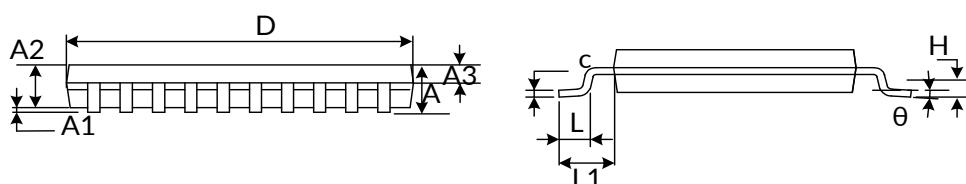
H. All parameters and waveforms are not applicable to all devices.

**Figure 3. Load Circuit and Voltage Waveforms**

## 10 PACKAGE OUTLINE DIMENSIONS TSSOP20<sup>(4)</sup>



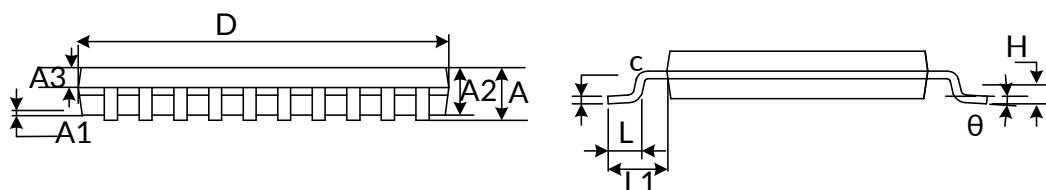
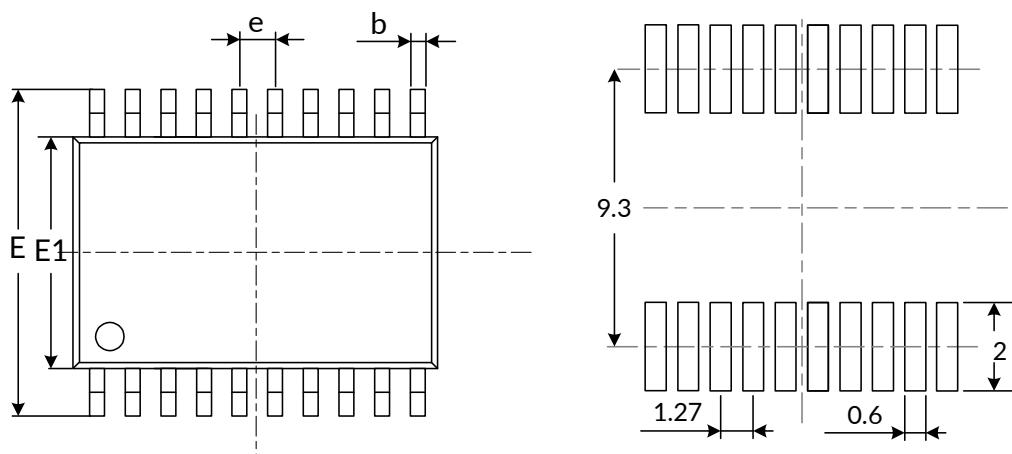
RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
A3	0.390	0.490	0.015	0.020
b	0.200	0.290	0.008	0.011
c	0.130	0.170	0.005	0.007
D <sup>(1)</sup>	6.400	6.600	0.252	0.260
E <sup>(1)</sup>	4.300	4.500	0.169	0.177
E1	6.200	6.600	0.244	0.260
e	0.650(BSC) <sup>(2)</sup>		0.026(BSC) <sup>(2)</sup>	
L	0.450	0.750	0.018	0.030
H	0.250(TYP)		0.010(TYP)	
θ	0°	8°	0°	8°
L1	1.00(REF) <sup>(3)</sup>		0.039(REF) <sup>(3)</sup>	

NOTE:

1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. REF is the abbreviation for Reference.
4. This drawing is subject to change without notice.

**SOP20<sup>(4)</sup>**


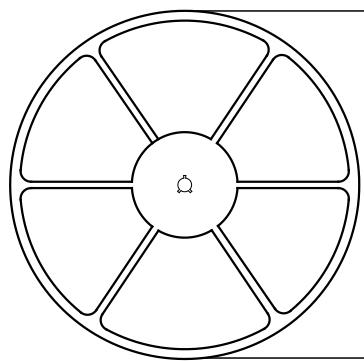
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A <sup>(1)</sup>		2.650		0.104
A1	0.100	0.300	0.004	0.012
A2	2.250	2.350	0.089	0.093
A3	0.970	1.070	0.038	0.042
b	0.390	0.470	0.015	0.019
c	0.250	0.290	0.010	0.011
D <sup>(1)</sup>	12.700	12.900	0.500	0.508
E	10.100	10.500	0.398	0.413
E1 <sup>(1)</sup>	7.400	7.600	0.291	0.299
e	1.270(BSC) <sup>(2)</sup>		0.050(BSC) <sup>(2)</sup>	
L	0.700	1.000	0.028	0.039
H	0.250(TYP)		0.010(TYP)	
θ	0°	8°	0°	8°
L1	1.400(REF) <sup>(3)</sup>		0.055(REF) <sup>(3)</sup>	

**NOTE:**

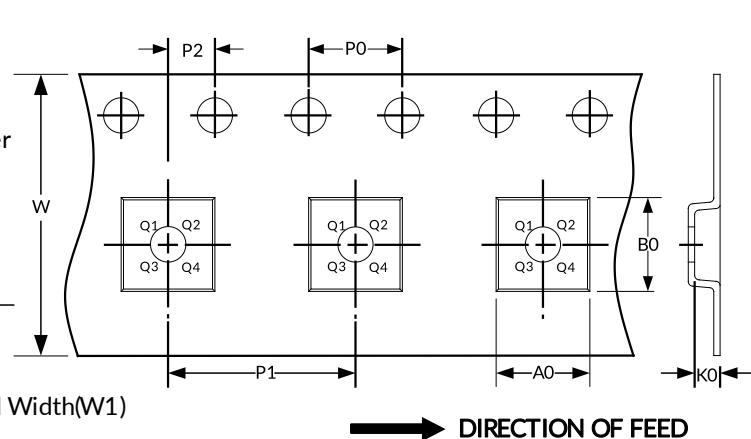
1. Plastic or metal protrusions of 0.15mm maximum per side are not included.
2. BSC (Basic Spacing between Centers), "Basic" spacing is nominal.
3. REF is the abbreviation for Reference.
4. This drawing is subject to change without notice.

## 11 TAPE AND REEL INFORMATION

### REEL DIMENSIONS



### TAPE DIMENSION



NOTE: The picture is only for reference. Please make the object as the standard.

### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width (mm)	$A_0$ (mm)	$B_0$ (mm)	$K_0$ (mm)	$P_0$ (mm)	$P_1$ (mm)	$P_2$ (mm)	$W$ (mm)	Pin1 Quadrant
TSSOP20	13"	12.4	6.75	6.95	1.20	4.0	8.0	2.0	16.0	Q1
SOP20	13"	24.4	10.75	13.55	2.65	4.0	12.0	2.0	24.0	Q1

NOTE:

1. All dimensions are nominal.
2. Plastic or metal protrusions of 0.15mm maximum per side are not included.

## IMPORTANT NOTICE AND DISCLAIMER

Jiangsu RUNIC Technology Co., Ltd. will accurately and reliably provide technical and reliability data (including data sheets), design resources (including reference designs), application or other design advice, WEB tools, safety information and other resources, without warranty of any defect, and will not make any express or implied warranty, including but not limited to the warranty of merchantability Implied warranty that it is suitable for a specific purpose or does not infringe the intellectual property rights of any third party.

These resources are intended for skilled developers designing with RUNIC products You will be solely responsible for: (1) Selecting the appropriate products for your application; (2) Designing, validating and testing your application; (3) Ensuring your application meets applicable standards and any other safety, security or other requirements; (4) RUNIC and the RUNIC logo are registered trademarks of RUNIC INCORPORATED. All trademarks are the property of their respective owners; (5) For change details, review the revision history included in any revised document. The resources are subject to change without notice. Our company will not be liable for the use of this product and the infringement of patents or third-party intellectual property rights due to its use.